

SHREE RAMKRISHNA INSTITUTE OF SCIENCE AND TECHNOLOGY

**LESSON PLAN**

SESSION : 2014-15  
 SEMESTER : 3rd  
 DEPARTMENT: **ETCE ENGINEERING**  
 SUBJECT : **DIGITAL ELECTRONICS**  
 TEACHER : ABHISHEK DAS(A.D)/ RUPESH SHARMA(R.S)

WEEK	TOPIC	SUB-TOPIC
1	Number system & basic logic gate	a> Introduction to number system b> Conversion among number system c> O.C & S.C impedance, image impedance d> 1's & 2's compliment e> Logic gates(AND,OR,NOR,XOR,XNOR,,BUFFRER)
2	BOOLEAN ALGEBRA	a> De Morgan's theorem b> Karnaugh map technique c> Simplification of Bolean expression d> Quin Mclusky method e> Realisation of Bolean expression
3/4	COMBINATIONAL LOGIC CIRCUIT	a> Arithmetic circuit(HA,FA,HS,FS,P/S ADDER) b> Code converter , encoder, decoder c> Parity generator & checker
5/6	SEQUENTIAL LOGIC CIRCUIT	a>flip flop b> counter d> Register
7	Memory device	a> RAM,ROM,PROM,EPROM,EEPROM,FLASH MEMORY b> PLA LOGIC
8	DATA CONVERTER	a> ADC c> DAC
9	LOGIC FAMILIES	TTL,ECL,CMOS,LOGIC HAZARDS,7400 TTL, CD 4000